

PRELIMINARY AMENDMENT

Serial Number: 10/815,563

Filing Date: March 31, 2004

Title: CLOCK RECOVERY METHODS AND APPARATUS

Assignee: Intel Corporation

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Dkt: 884.B93US1 (INTEL)

IN THE SPECIFICATION

Please amend the specification as follows:

Please amend the paragraph beginning at page 4, line 17 as follows:

As will be described later, both the source and destination devices also include another clock, referred to herein as a “substantially synchronized clock,” which is associated with each the device’s network interface. Timestamps generated from the substantially synchronized clocks are used to account for the variations in delay that may occur as a result of network contention, routing, and/or other delay-producing factors. These inventive concepts will be described in detail below, using various example systems and device configurations to illustrate application of the concepts.